



Lawrence Berkeley
National Laboratory

Preliminary Agenda

DOE Workshop on Energy Efficient Electronics

Date: Thursday, March 24, 2016

Location: LBNL 66-Auditorium

Arrival and continental breakfast

7:45 – 8:15

Motivation session

8:15 – 8:30	M. Witherell, LBNL	Welcome
8:30 – 9:00	M. Johnson, AMO	AMO Overview and Perspective on semiconductor
9:00 – 9:30	W. Harrod, ASCR	ASCR Overview and Perspective on semiconductor
9:30 – 9:50	R. McCormick, Sandia	Opportunities for a DOE program in energy efficient electronics
9:50 – 10:20		Break
10:20 – 10:50	J. Golda, Intel	DOE Lab's and advanced semiconductor manufacturing research
10:50 – 11:20	T. King-Liu, UCB	Device challenges and opportunities talk
11:20 – 11:40	R. Nair, IBM	System challenges and opportunities talk, Part 1
11:40 – 12:00	S. Borkar, Intel	System challenges and opportunities talk, Part 2
12:00 – 1:30		Working lunch

Focus group session

1:30 – 3:00	D. Armbrust, Silicon Catalyst	Session A: Precompetitive manufacturing challenges
	S. Salahuddin, UCB	Session B: Device technology impact on energy
	S. Borkar, Intel	Session C: Information technology impact on energy
3:00 – 3:30		Break
3:30 – 4:00	S. Borkar	Report out and discussion from Focus Group C
4:00 – 4:30	D. Armbrust	Report out and discussion from Focus Group A
4:30 – 5:00	S. Salahuddin	Report out and discussion from Focus Group B

Closeout session

5:00 – 5:30		Wrap up discussion
6:00 – 8:00		Working dinner

Participants:

Albert Wang, SanDisk (A)
Andreas Roelofs, ANL (C)
Andy Hawryluk, Ultratech (B)
Anton Devilliers, TEL (A)
Bert de Jong, LBL (B)
Bill Harrod, DOE (ASCR) (C)
Bob Akins, ASML/Cymer (A)
Bruno LaFontaine, ASML/BRION (A)
Bryan Kasprowicz, Photronics (A)
Connie Chang-Hasnain, UC Berkeley (B)
Dan Armbrust, Silicon Catalyst (A)
Danping Peng, TSMC North America (B)
Dean Klein, Micron (A)
Deepak Nayak, GlobalFoundries (B)
Dev Shenoy, DOE (AMO) (B)
Dileep Bhandarkar, Qualcomm (C)
Doug Barlage, University of Alberta (B)
Eli Yablonovitch, UC Berkeley (B)
Ellie Yieh, AMAT (B)
Gerd Ceder, LBL (B)
Gil Vandentop, Intel (A)
Greg Yeric, ARM (B)
Horst Simon, LBL (C)
Janice Golda, Intel (A)
John Aidon, Sandia (C)
John Bell, LBL (C)
John Shalf, LBL (C)
Kathy Yelick, LBL (C)
Keren Bergman, Columbia (C)
Li-Jin Chen, TSMC North America (A)
Marie Tripp, Intel (A)
Mark Fissel, Lam Research (B)
Mark Johnson, DOE (AMO) (B)
Matt Marinella, Sandia (C)
Michael Schulte, AMD (C)
Nag Patibandla, AMAT (A)
Patrick Naulleau, LBL (A)
Peter Denes, LBL (C)
Peter Fischer, LBL (B)
Ramamoorthy Ramesh, LBL (B)
Ravi Nair, IBM (C)
Rick McCormick, Sandia (C)
Rick Sandstrom, ASML/Cymer (A)
Rob Ivester, DOE (AMO) (A)
Russ Hudyma, Navatar (A)
Sayeef Salahuddin, UC Berkeley (B)
Shekhar Y Borkar, Intel (C)
Stan Stakowsky, KLA-Tencor (A)
Stefan Wurm, Consultant (A)
Subash Mitra, Stanford (C)
Thomas Schenkel, LBL (A)
Toni Taylor, LANL (B)
Tony Lentine, Sandia (C)
Tsu-Jae King-Liu, UC Berkeley (A)
Wilfried Haensch, IBM (C)